

O 4-18-00

04/17/00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of: Wendell P. Noble et al.

Title: CIRCUIT AND METHOD FOR A FOLDED BIT LINE MEMORY CELL WITH VERTICAL TRANSISTOR AND TRENCH CAPACITOR

Attorney Docket No.: 303.379US2

JC678 U.S. PTO  
 04/17/00  
**08/939,742**  
 04/17/00

**PATENT APPLICATION TRANSMITTAL****BOX PATENT APPLICATION**Assistant Commissioner for Patents  
Washington, D.C. 20231

We are transmitting herewith the following attached items and information (as indicated with an "X"):

- DIVISIONAL of prior Patent Application No. 08/939,742 (under 37 CFR § 1.53(b)) comprising:
  - Specification ( 22 pgs, including claims numbered 1 through 26 and a 1 page Abstract).
  - Formal Drawing(s) ( 16 sheets).
  - Copy of signed Combined Declaration and Power of Attorney ( 6 pgs) from prior application.
  - Incorporation by Reference: *The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.*
  - Check in the amount of \$690.00 to pay the filing fee.
- Prior application is assigned of record to Micron Technology, Inc..
- Information Disclosure Statement ( 1 pgs), Form 1449 ( 13 pgs). References NOT enclosed, cited in prior application.
- Preliminary Amendment ( 1 pgs).
- Return postcard.

The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	7 - 20 =	0	x 18 =	\$0.00
INDEPENDENT CLAIMS	2 - 3 =	0	x 78 =	\$0.00
[ ] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$690.00
	TOTAL			\$690.00

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)By: Daniel J. Kluth  
Atty: Daniel J. Kluth  
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I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Box Patent Application, Washington, D.C. 20231.

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Date of Deposit: April 17, 2000